EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	64	(core near3 (voltage power)) with ((enough sufficient) near6 (operat$3 activ$5 execut$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 13:43
L2	13	1.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 13:46
L3	1500	713/320.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/04/03 14:41
L4	934	(voltage adj1 regulator$1) with (mode near8 (chang$3 switch$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 14:01
L5	0	("core voltage" near5 (low$2 reduc$3)) same 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 14:38
L6	5	("core voltage" near5 (low$2 reduc$3)) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 14:38
L7	2744	713/300.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/04/03 14:41
L8	79	(stop$3 or disable$1) near4 clock same ((reduc$3 or lower$3) near8 core)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 14:53
L9	5	8.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/03 14:53